

1. General Description

The EM74HC367; EM74HCT367 is a hex buffer/line driver with 3-state outputs controlled by the output enable inputs ($\overline{\text{nOE}}$). A HIGH on $\overline{\text{nOE}}$ causes the outputs to assume a high impedance OFF-state. Inputs include clamp diodes. This enables the use of current limiting resistors to interface inputs to voltages in excess of V_{CC} .

2. Features and Benefits

- Wide supply voltage range from 2.0 V to 6.0 V
- High noise immunity
- CMOS low power dissipation
- Latch-up performance exceeds 250 mA
- Complies with JEDEC standards:
 - JESD8C (2.7 V to 3.6 V)
 - JESD7A (2.0 V to 6.0 V)
- Input levels:
 - For EM74HC367: CMOS level
 - For EM74HCT367: TTL level
- ESD protection:
 - HBM ANSI/ESDA/JEDEC JS-001 Class 2 exceeds 3500 V
 - CDM ANSI/ESDA/JEDEC JS-002 Class C3 exceeds 2000 V
- Multiple package options

EM74HC367; EM74HCT367

Hex buffer/line driver; 3-state

3. Ordering Information

Table 1. Ordering information

Type number	Package		
	Name	Description	Quantity
EM74HC367D	SOP-16L	plastic small outline package; 16 leads; body width 3.9 mm	3000
EM74HCT367D			
EM74HC367PW	TSSOP-16L	plastic thin shrink small outline package; 16 leads; body width 4.4 mm	3000
EM74HCT367PW			

4. Function Diagram

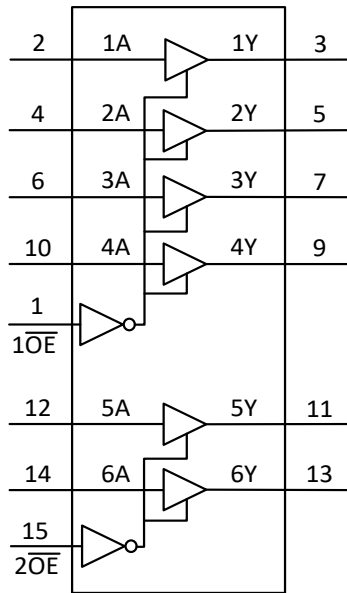


Fig. 1. Functional diagram

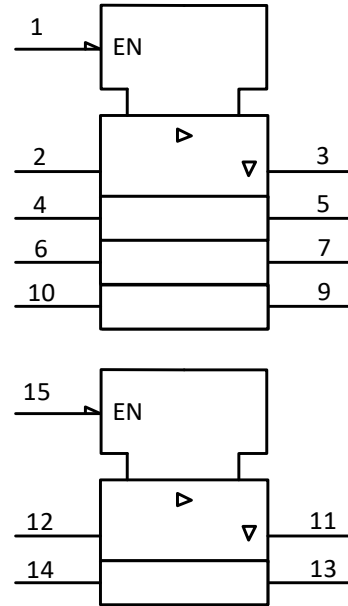


Fig. 2. IEC logic symbol

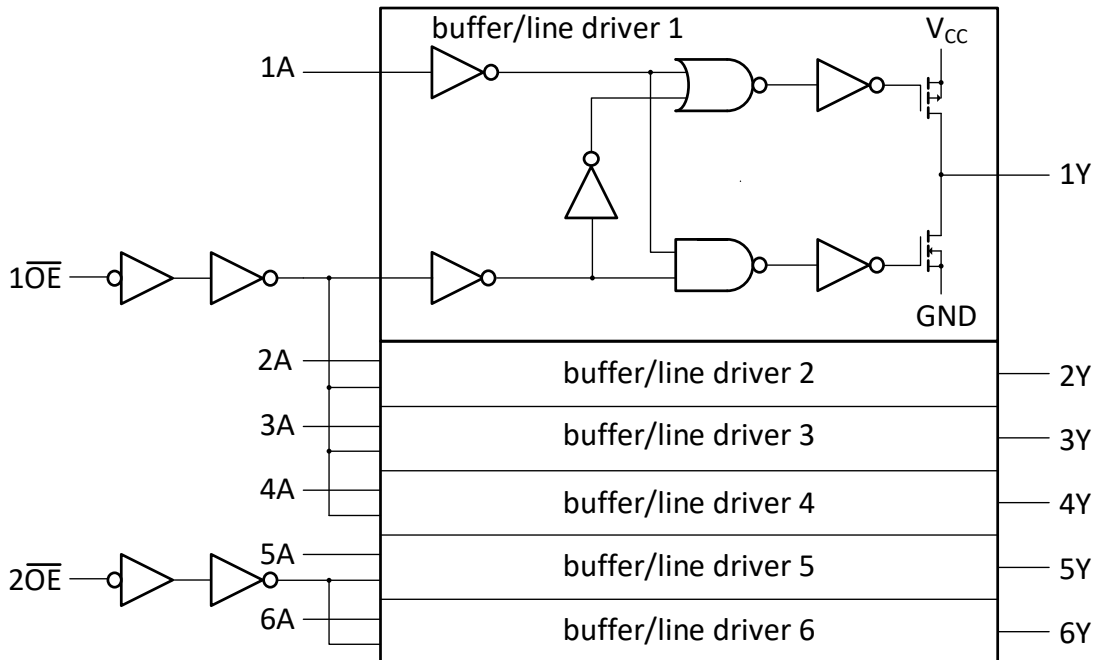


Fig. 3. Logic diagram