

1. General Description

The EM74HC366; EM74HCT366 is a hex inverting buffer/line driver with 3-state outputs controlled by the output enable inputs ($\overline{OEn}$). A HIGH on $\overline{OEn}$ causes the outputs to assume a high impedance OFF-state. Inputs include clamp diodes. This enables the use of current limiting resistors to interface inputs to voltages in excess of V_{CC} .

2. Features and Benefits

- Wide supply voltage range from 2.0 V to 6.0 V
- High noise immunity
- CMOS low power dissipation
- Latch-up performance exceeds 250 mA
- Complies with JEDEC standards:
 - JESD8C (2.7 V to 3.6 V)
 - JESD7A (2.0 V to 6.0 V)
- Input levels:
 - For EM74HC366: CMOS level
 - For EM74HCT366: TTL level
- ESD protection:
 - HBM ANSI/ESDA/JEDEC JS-001 Class 2 exceeds 3500 V
 - CDM ANSI/ESDA/JEDEC JS-002 Class C3 exceeds 2000 V
- Multiple package options

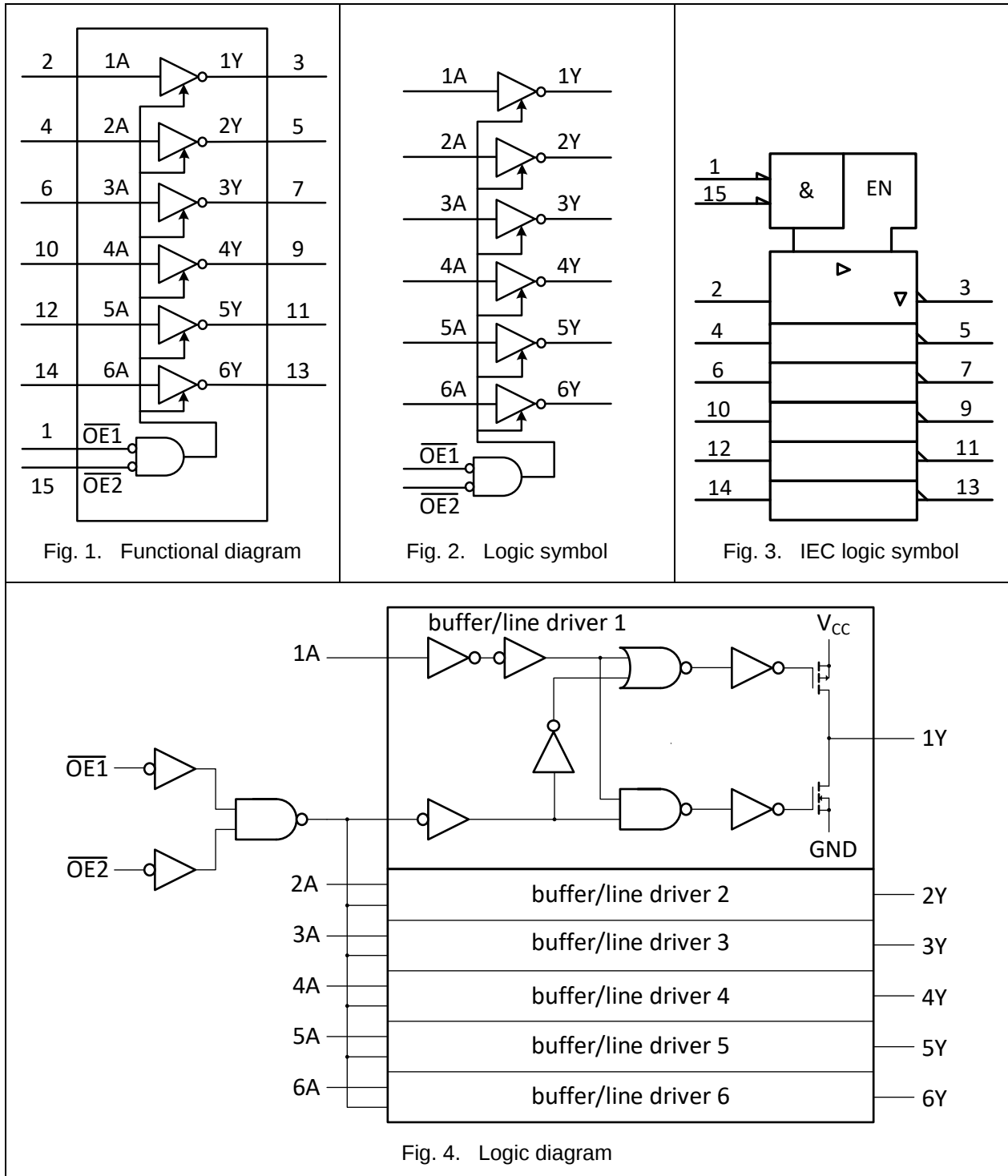
EM74HC366; EM74HCT366**Hex buffer/line driver; 3-state; inverting**

3. Ordering Information

Table 1. Ordering information

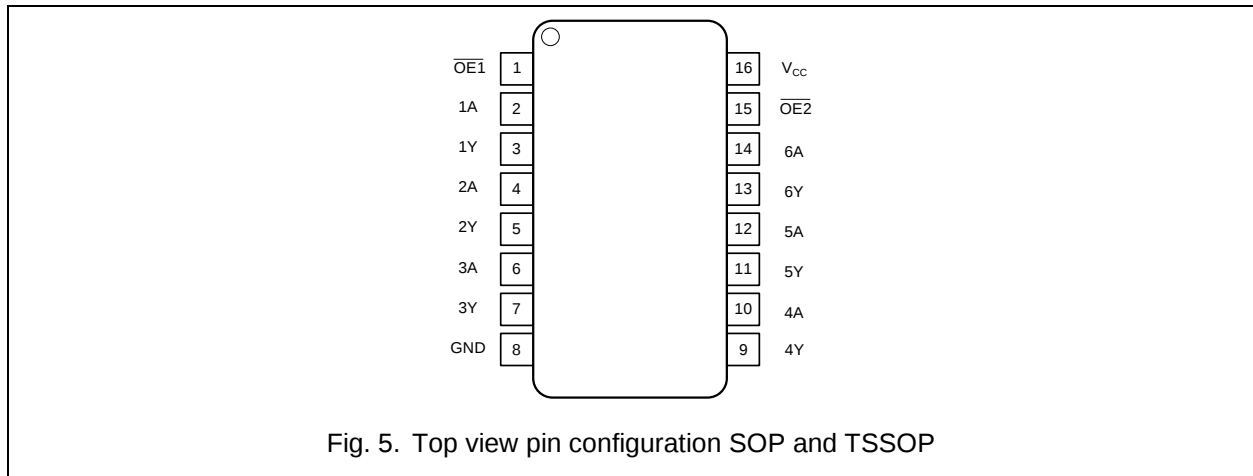
Type number	Package		
	Name	Description	Quantity
EM74HC366D	SOP-16L	plastic small outline package; 16 leads; body width 3.9 mm	3000
EM74HCT366D			
EM74HC366PW	TSSOP-16L	plastic thin shrink small outline package; 16 leads; body width 4.4 mm	3000
EM74HCT366PW			

4. Function Diagram



5. Pinning Information

5.1. Pinning



5.2. Pin description

Table 2. Pin description

Symbol	Pin	Description
$\overline{\text{OE1}}$	1	output enable input 1 (active LOW)
1A	2	data input 1
1Y	3	data output 1
2A	4	data input 2
2Y	5	data output 2
3A	6	data input 3
3Y	7	data output 3
GND	8	ground (0 V)
4Y	9	data output 4
4A	10	data input 4
5Y	11	data output 5
5A	12	data input 5
6Y	13	data output 6
6A	14	data input 6
$\overline{\text{OE2}}$	15	output enable input 2 (active LOW)
V _{CC}	16	supply voltage

6. Functional Description

Table 3. Function table

H = HIGH voltage level; L = LOW voltage level; X = don't care; Z = high-impedance OFF-state.

Control		Input	Output
$\overline{OE1}$	$\overline{OE2}$	nA	nY
L	L	L	H
L	L	H	L
X	H	X	Z
H	X	X	Z

7. Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only.

Table 4. Absolute Maximum Ratings

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND.

Symbol	Parameter	Conditions	Min	Max	Unit
V_{CC}	supply voltage		-0.5	7.0	V
I_{IK}	input clamping current	$V_I < -0.5\text{ V}$ or $V_I > V_{CC} + 0.5\text{ V}$ [1]		± 20	mA
I_{OK}	output clamping current	$V_O < -0.5\text{ V}$ or $V_O > V_{CC} + 0.5\text{ V}$ [1]		± 20	mA
I_O	output current	$V_O = -0.5\text{ V}$ to $(V_{CC} + 0.5\text{ V})$		± 35	mA
I_{CC}	supply current			70	mA
I_{GND}	ground current		-70		mA
P_{tot}	total power dissipation			500	mW
T_{stg}	storage temperature		-65	150	°C

[1] The input and output voltage ratings may be exceeded if the input and output current ratings are observed.

8. Recommended Operating Conditions

The Recommended Operating Conditions table defines the conditions for actual device operation. Recommended operating conditions are specified to ensure optimal performance to the datasheet specifications. EnergyMath does not recommend exceeding them or designing to Absolute Maximum Ratings.

Table 5. Recommended Operating Conditions

Symbol	Parameter	Conditions	EM74HC366			EM74HCT366			Unit
			Min	Typ	Max	Min	Typ	Max	
V_{CC}	supply voltage		2.0	5.0	6.0	4.5	5.0	5.5	V
V_I	input voltage		0		V_{CC}	0		V_{CC}	V
V_O	output voltage		0		V_{CC}	0		V_{CC}	V
T_{amb}	ambient temperature		-40		125	-40		125	°C
$\Delta t/\Delta V$	input transition rise and fall rate	$V_{CC} = 2.0\text{ V}$			625				ns/V
		$V_{CC} = 4.5\text{ V}$		1.67	139		1.67	139	ns/V
		$V_{CC} = 6.0\text{ V}$			83				ns/V

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9.Static Characteristics

Table 6. Static characteristics

At recommended operating conditions. Voltages are referenced to GND (ground = 0 V). Typical values measured at $T_{amb} = 25^{\circ}\text{C}$ (unless otherwise noted).

Symbol	Parameter	Conditions	-40 °C to +85 °C			-40 °C to +125 °C		Unit
			Min	Typ	Max	Min	Max	
EM74HC366								
V _{IH}	HIGH-level input voltage	V _{CC} = 2.0 V	1.5			1.5		V
		V _{CC} = 4.5 V	3.15			3.15		V
		V _{CC} = 6.0 V	4.2			4.2		V
V _{IL}	LOW-level input voltage	V _{CC} = 2.0 V			0.5		0.5	V
		V _{CC} = 4.5 V			1.35		1.35	V
		V _{CC} = 6.0 V			1.8		1.8	V
V _{OH}	HIGH-level output voltage	V _I = V _{IH} or V _{IL}						
		I _O = -20 μA; V _{CC} = 2.0 V	1.9			1.9		V
		I _O = -20 μA; V _{CC} = 4.5 V	4.4			4.4		V
		I _O = -20 μA; V _{CC} = 6.0 V	5.9			5.9		V
		I _O = -6.0 mA; V _{CC} = 4.5 V	3.84			3.7		V
		I _O = -7.8 mA; V _{CC} = 6.0 V	5.34			5.2		V
V _{OL}	LOW-level output voltage	V _I = V _{IH} or V _{IL}						
		I _O = 20 μA; V _{CC} = 2.0 V			0.1		0.1	V
		I _O = 20 μA; V _{CC} = 4.5 V			0.1		0.1	V
		I _O = 20 μA; V _{CC} = 6.0 V			0.1		0.1	V
		I _O = 6.0 mA; V _{CC} = 4.5 V			0.33		0.4	V
		I _O = 7.8 mA; V _{CC} = 6.0 V			0.33		0.4	V
I _I	input leakage current	V _I = V _{CC} or GND ; V _{CC} = 6.0 V			±1		±1	μA
I _{OZ}	OFF-state output current	V _I = V _{IH} or V _{IL} ; V _{CC} = 6.0 V ; V _O = V _{CC} or GND			±5		±10	μA
I _{CC}	supply current	V _I = V _{CC} or GND ; I _O = 0 A ; V _{CC} = 6.0 V			20		40	μA
C _I	input capacitance			7				pF

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Symbol	Parameter	Conditions	-40 °C to +85 °C			-40 °C to +125 °C		Unit
			Min	Typ	Max	Min	Max	
EM74HCT366								
V _{IH}	HIGH-level input voltage	V _{CC} = 4.5 V to 5.5 V	2.0			2.0		V
V _{IL}	LOW-level input voltage	V _{CC} = 4.5 V to 5.5 V			0.8		0.8	V
V _{OH}	HIGH-level output voltage	V _I = V _{IH} or V _{IL} ; V _{CC} = 4.5 V						
		I _O = -20 μA;	4.4		4.4		V	
		I _O = -6.0 mA;	3.84		3.7		V	
V _{OL}	LOW-level output voltage	V _I = V _{IH} or V _{IL} ; V _{CC} = 4.5 V						
		I _O = 20 μA;			0.1		0.1	V
		I _O = 6.0 mA;			0.33		0.4	V
I _I	input leakage current	V _I = V _{CC} or GND ; V _{CC} = 5.5 V			±1		±1	μA
I _{OZ}	OFF-state output current	V _I = V _{IH} or V _{IL} ; V _{CC} = 5.5 V ; V _O = V _{CC} or GND			±5		±10	μA
I _{CC}	supply current	V _I = V _{CC} or GND; I _O = 0 A; V _{CC} = 5.5 V			20		40	μA
ΔI _{CC}	additional supply current	per pin ; V _I = V _{CC} - 2.1 V; I _O = 0 A; other inputs at V _{CC} or GND; V _{CC} = 4.5 V to 5.5 V			1.55		1.85	mA
C _I	input capacitance			10				pF

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10. Dynamic Characteristics

Table 7. Dynamic characteristics

Voltages are referenced to GND (ground = 0 V); for test circuit see Fig. 8. Typical values measured at $T_{amb} = 25^{\circ}\text{C}$ (unless otherwise noted).

Symbol	Parameter	Conditions	-40 °C to +85 °C			-40 °C to +125 °C		Unit
			Min	Typ	Max	Min	Max	
EM74HC366								
t _{pd}	propagation delay	nA to nY; see Fig. 6 [1]						
		V _{CC} = 2.0 V			35		40	ns
		V _{CC} = 4.5 V			20		25	ns
		V _{CC} = 6.0 V			15		20	ns
t _{en}	enable time	$\overline{\text{OEn}}$ to nY; see Fig. 7 [2]						
		V _{CC} = 2.0 V			35		40	ns
		V _{CC} = 4.5 V			20		25	ns
		V _{CC} = 6.0 V			15		20	ns
t _{dis}	disable time	$\overline{\text{OEn}}$ to nY; see Fig. 7 [3]						
		V _{CC} = 2.0 V			35		40	ns
		V _{CC} = 4.5 V			20		25	ns
		V _{CC} = 6.0 V			15		20	ns
t _t	transition time	see Fig. 6 [4]						
		V _{CC} = 2.0 V			9		11	ns
		V _{CC} = 4.5 V			6		8	ns
		V _{CC} = 6.0 V			4		5	ns
C _{PD}	power dissipation capacitance	C _L = 15 pF; f = 1 MHz; V _I = GND to V _{CC} [5]		18				pF

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Symbol	Parameter	Conditions	-40 °C to +85 °C			-40 °C to +125 °C		Unit
			Min	Typ	Max	Min	Max	
EM74HCT366								
t _{pd}	propagation delay	nA to nY; V _{CC} = 4.5 V; see Fig. 6 [1]			20		25	ns
t _{en}	enable time	$\overline{OEn}$ to nY; V _{CC} = 4.5 V; see Fig. 7 [2]			20		25	ns
t _{dis}	disable time	$\overline{OEn}$ to nY; V _{CC} = 4.5 V; see Fig. 7 [3]			20		25	ns
t _t	transition time	V _{CC} = 4.5 V; see Fig. 6 [4]			6		8	ns
C _{PD}	power dissipation capacitance	C _L = 15 pF; f = 1 MHz; V _I = GND to (V _{CC} -1.5 V) [5]		37				pF

[1] t_{pd} is the same as t_{PHL} and t_{PLH} .

[2] t_{en} is the same as t_{PZH} and t_{PZL} .

[3] t_{dis} is the same as t_{PLZ} and t_{PHZ} .

[4] t_t is the same as t_{THL} and t_{TLH} .

[5] C_{PD} is used to determine the dynamic power dissipation (P_D in μW).

$P_D = C_{PD} \times V_{CC}^2 \times f_i \times N + \Sigma(C_L \times V_{CC}^2 \times f_o)$ where:

f_i = input frequency in MHz;

f_o = output frequency in MHz;

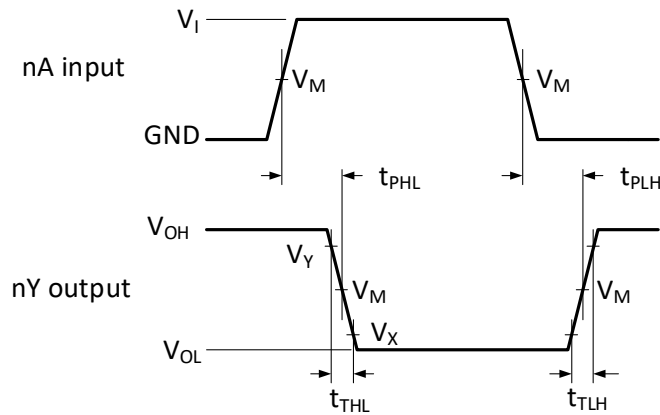
C_L = output load capacitance in pF;

V_{CC} = supply voltage in V;

N = number of inputs switching;

$\Sigma(C_L \times V_{CC}^2 \times f_o)$ = sum of outputs.

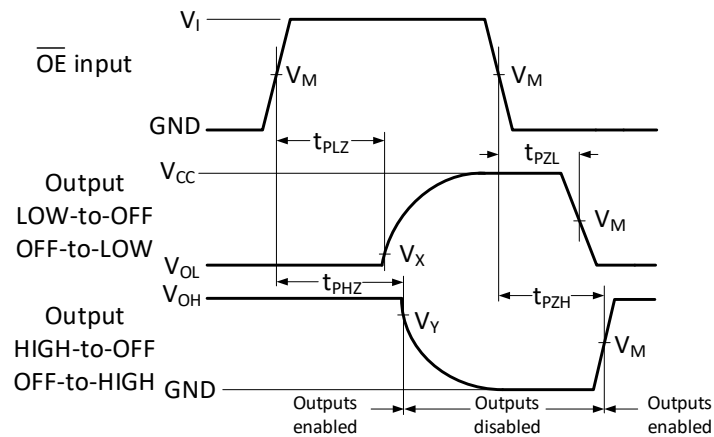
10.1. Waveforms and test circuit



Measurement points are given in Table 8.

VOL and VOH are typical output voltage levels that occur with the output load.

Fig. 6. The input nA to output nY propagation delays



Measurement points are given in Table 8.

VOL and VOH are typical output voltage levels that occur with the output load.

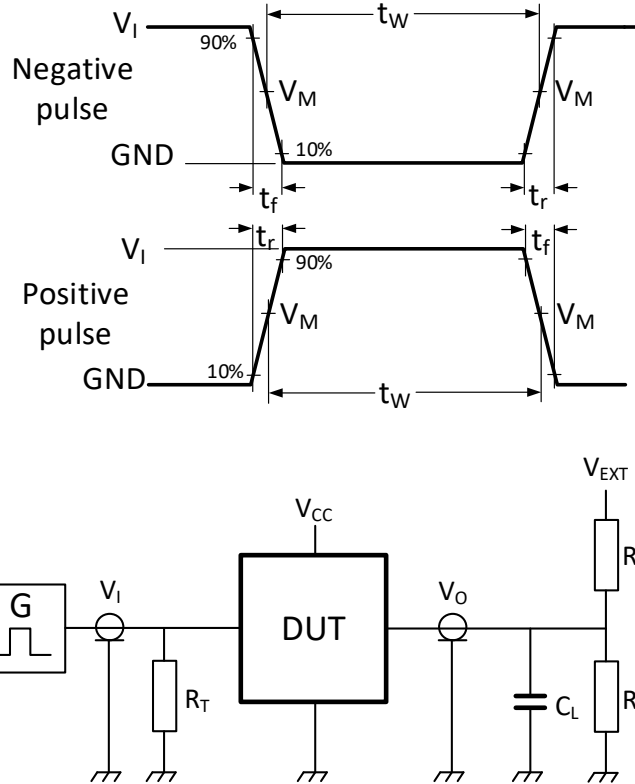
Fig. 7. 3-state; inverting enable and disable times

Table 8. Measurement points

Type	Input	Output		
	VM	VM	VX	VY
EM74HC366	0.5VCC	0.5VCC	0.1VCC	0.9VCC
EM74HCT366	1.3 V	1.3 V	0.1VCC	0.9VCC

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Test data is given in Table 9.

Definitions for test circuit:

R_L = Load resistance.

C_L = Load capacitance including jig and probe capacitance.

R_T = Termination resistance should be equal to the output impedance Z_o of the pulse generator.

V_{EXT} = External voltage for measuring switching times.

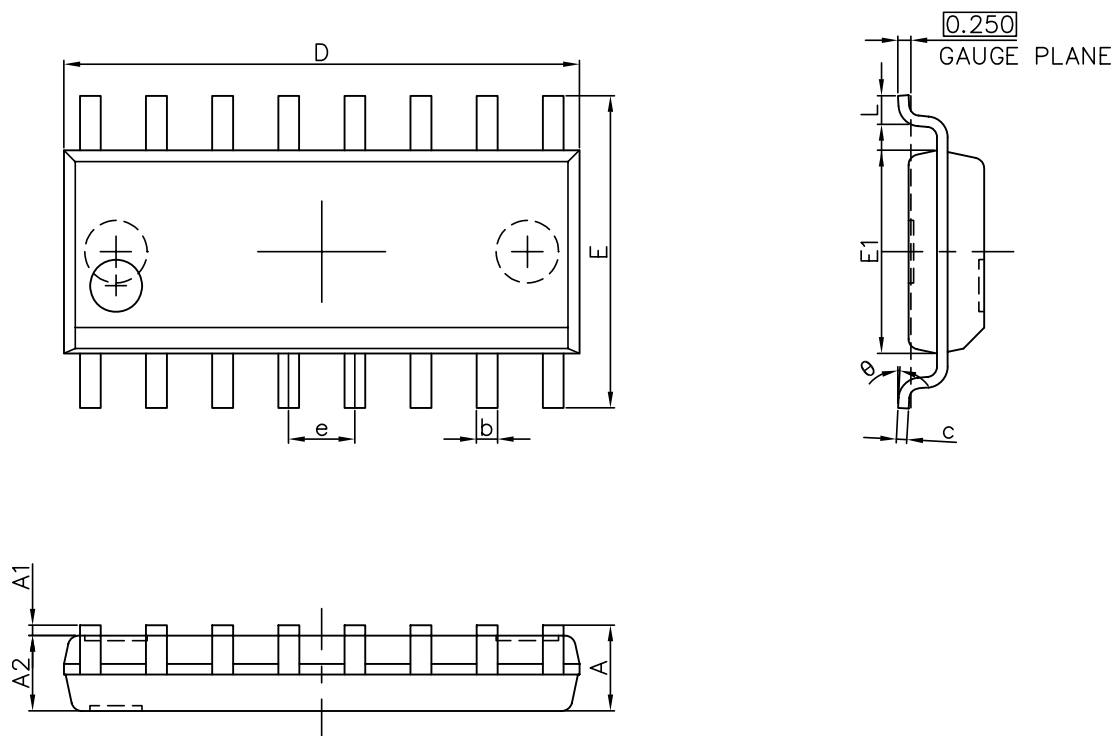
Fig. 8. Test circuit for measuring switching times

Table 9. Test data

Type	Input		Load		V_{EXT}		
	V_I	$t_r = t_f$	C_L	R_L	t_{PLH}, t_{PHL}	t_{PZH}, t_{PHZ}	t_{PZL}, t_{PLZ}
EM74HC366	V_{CC}	≤ 2.5 ns	15 pF	500 Ω	open	GND	$2V_{CC}$
EM74HCT366	3 V	≤ 2.5 ns	15 pF	500 Ω	open	GND	$2V_{CC}$

11. Package Outline

SOP-16L

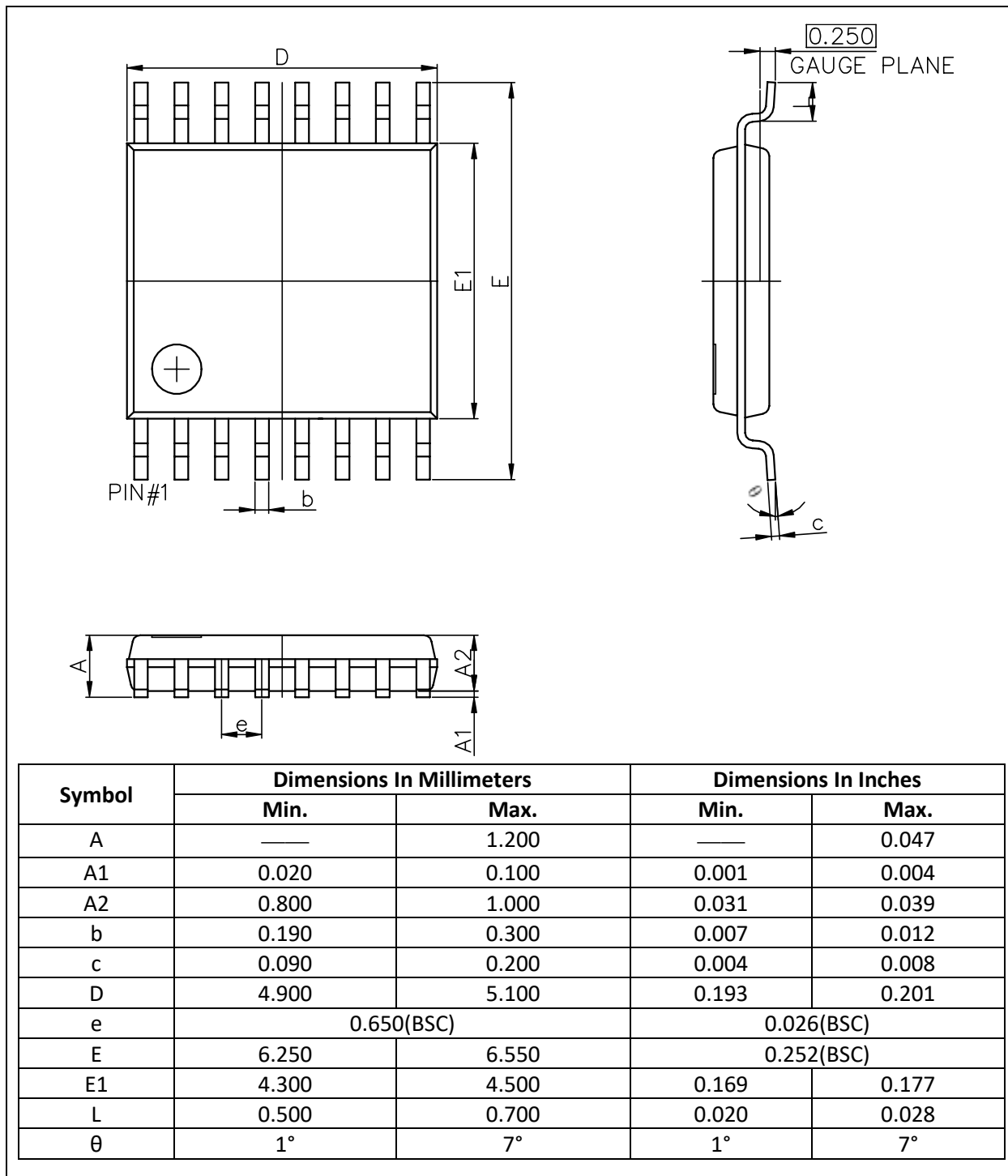


Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	—	1.750	—	0.069
A1	0.150	0.250	0.006	0.010
A2	1.400	1.500	0.055	0.059
b	0.330	0.510	0.013	0.020
c	0.170	0.250	0.007	0.010
D	9.800	10.000	0.386	0.394
e	1.270(BSC)		0.050(BSC)	
E	5.900	6.100	0.232	0.240
E1	3.800	4.000	0.150	0.157
L	0.400	1.270	0.016	0.050
θ	0°	8°	0°	8°

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TSSOP-16L



12. Abbreviations

Table 10. Abbreviations

Acronym	Description
CMOS	Complementary Metal-Oxide Semiconductor
DUT	Device Under Test
ESD	ElectroStatic Discharge
HBM	Human Body Model
CDM	Charged Device Model
TTL	Transistor-Transistor Logic

13. Revision History

Table 11. Revision history

Document ID	Release Date	Data sheet status	Change notice	Supersedes
EM74HC_HCT366 Rev. 1.0	Aug 30, 2024	Product datasheet		