

## 1. General Description

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The EM74HC164A is an 8-bit serial-in/parallel-out shift register. The device features two serial data inputs (DSA and DSB), eight parallel data outputs (Q0 to Q7). Data is entered serially through DSA or DSB and either input can be used as an active HIGH enable for data entry through the other input. Data is shifted on the LOW-to-HIGH transitions of the clock (CP) input. A LOW on the master reset input ( $\overline{MR}$ ) clears the register and forces all outputs LOW, independently of other inputs. Inputs include clamp diodes. This enables the use of current limiting resistors to interface inputs to voltages in excess of  $V_{CC}$ .

## 2. Features and Benefits

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- Wide supply voltage range from 2.0 V to 6.0 V
- High noise immunity
- CMOS low power dissipation
- Gated serial data inputs
- Asynchronous master reset
- Latch-up performance exceeds 250 mA
- Complies with JEDEC standard:
  - JESD8C(2.7 V to 3.6 V)
  - JESD7A(2.0 V to 6.0 V)
- ESD protection:
  - HBM ANSI/ESDA/JEDEC JS-001 Class 3A exceeds 6500 V
  - CDM ANSI/ESDA/JEDEC JS-002 Class C3 exceeds 2000 V
- Multiple package options
- Specified from -40 °C to +85 °C and from -40 °C to +125 °C

### 3. Ordering Information

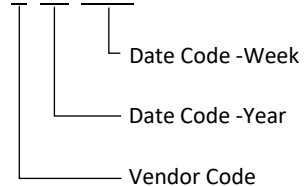
**Table 1. Ordering information**

Type number	Topside marking	Package		
		Name	Description	Quantity
EM74HC164AD	HC164A XYYWW	SOP-14L	plastic small outline package; 14 leads; body width 3.9 mm	3000
EM74HC164APW	HC164A XYYWW	TSSOP-14L	plastic thin shrink small outline package; 14 leads; body width 4.4 mm	3000

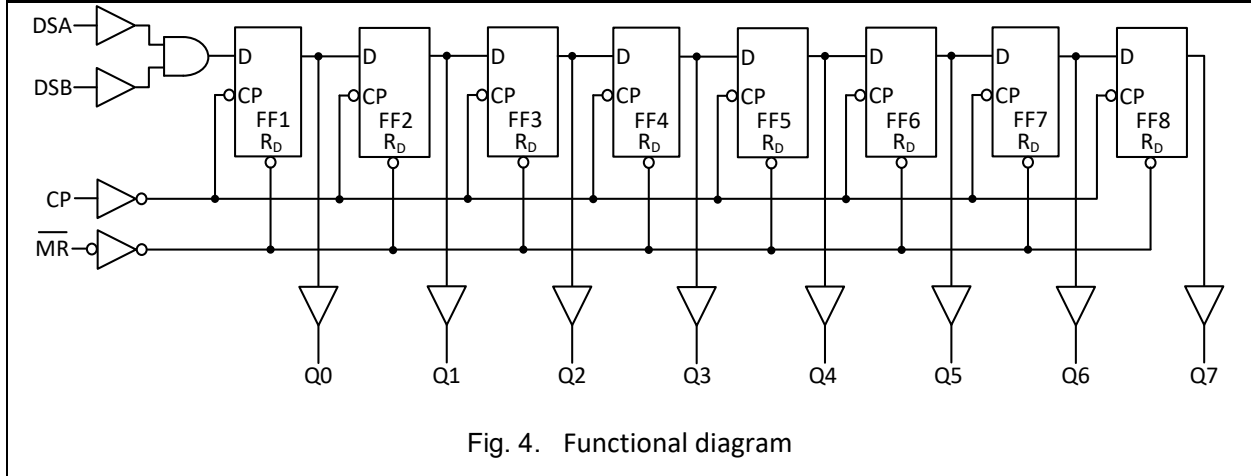
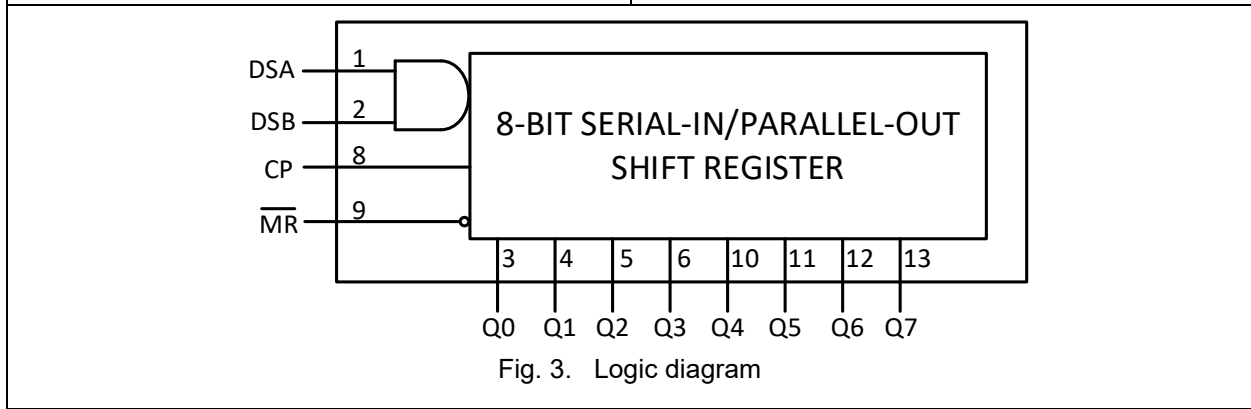
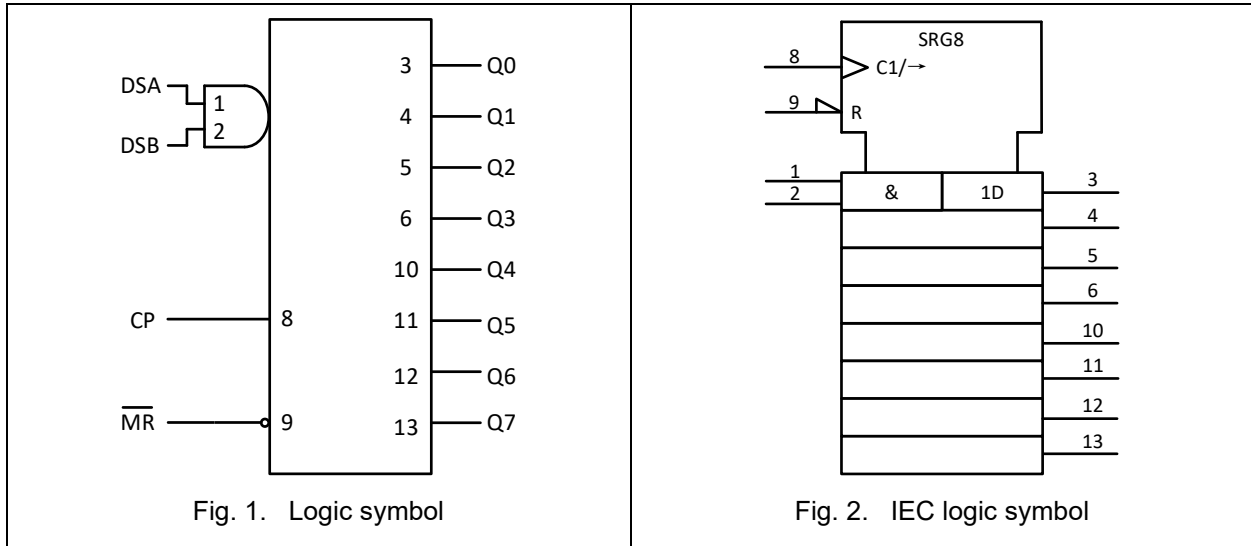
#### MARKING INFORMATION

NOTE: XYYWW = Vendor Code and Date Code.

X YY WW

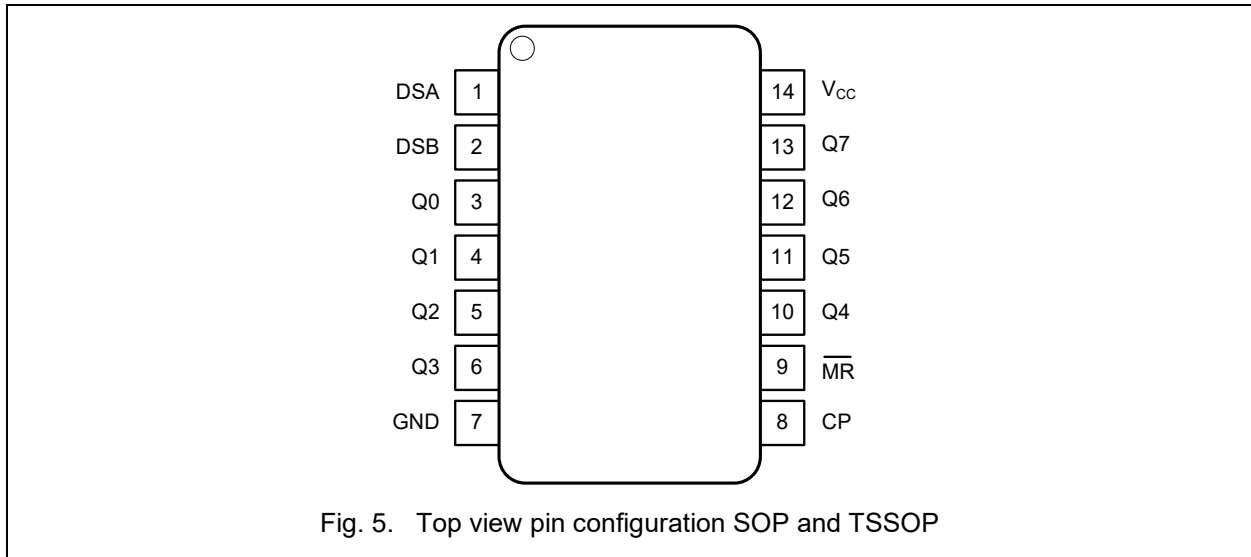


## 4. Function Diagram



## 5. Pinning Information

### 5.1. Pinning



### 5.2. Pin description

Table 2. Pin description

Symbol	Pin	Description
DSA	1	data input
DSB	2	data input
Q0, Q1, Q2, Q3, Q4, Q5, Q6, Q7	3, 4, 5, 6, 10, 11, 12, 13	output
CP	8	clock input (LOW-to-HIGH, edge-triggered)
MR	9	master reset input (active LOW)
V <sub>cc</sub>	14	supply voltage
GND	7	ground

## 6. Functional Description

**Table 3. Function table**

H = HIGH voltage level; h = HIGH voltage level one set-up time prior to the LOW-to-HIGH clock transition;  
 L = LOW voltage level; l = LOW voltage level one set-up time prior to the LOW-to-HIGH clock transition;  
 q = lower case letters indicate the state of the referenced input one set-up time prior to the LOW-to-HIGH clock transition; ↑ = LOW-to-HIGH transition; X = don't care

Operating modes	Input				Output	
	$\overline{\text{MR}}$	CP	DSA	DSB	Q0	Q1 to Q7
Reset(clear)	L	X	X	X	L	L to L
Shift	H	↑	l	l	L	q0 to q6
	H	↑	l	h	L	q0 to q6
	H	↑	h	l	L	q0 to q6
	H	↑	h	h	H	q0 to q6

## 7. Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only.

**Table 4. Absolute Maximum Ratings**

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND.

Symbol	Parameter	Conditions	Min	Max	Unit
V <sub>CC</sub>	supply voltage		-0.5	7.0	V
I <sub>IK</sub>	input clamping current	V <sub>O</sub> < -0.5 V or V <sub>O</sub> > V <sub>CC</sub> + 0.5 V [1]		±20	mA
I <sub>OK</sub>	output clamping current	V <sub>O</sub> < -0.5 V or V <sub>O</sub> > V <sub>CC</sub> + 0.5 V [1]		±20	mA
I <sub>O</sub>	output current	-0.5 V < V <sub>O</sub> < V <sub>CC</sub> + 0.5 V		±25	mA
I <sub>CC</sub>	supply current			50	mA
I <sub>GND</sub>	ground current		-50		mA
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> = -40 °C to + 125 °C		500	mW
T <sub>stg</sub>	storage temperature		-65	150	°C

[1] The input and output voltage ratings may be exceeded if the input and output current ratings are observed.

## 8. Recommended Operating Conditions

The Recommended Operating Conditions table defines the conditions for actual device operation. Recommended operating conditions are specified to ensure optimal performance to the datasheet specifications. EnergyMath does not recommend exceeding them or designing to Absolute Maximum Ratings.

**Table 5. Recommended Operating Conditions**

Symbol	Parameter	Conditions	EM74HC164A			Unit
			Min	Typ	Max	
V <sub>CC</sub>	supply voltage		2.0	5.0	6.0	V
V <sub>I</sub>	input voltage		0		V <sub>CC</sub>	V
V <sub>O</sub>	output voltage		0		V <sub>CC</sub>	V
T <sub>amb</sub>	ambient temperature		-40	+25	+125	°C
Δt/ΔV	input transition rise and fall rate	V <sub>CC</sub> = 2.0 V			625	ns/V
		V <sub>CC</sub> = 4.5 V		1.67	139	ns/V
		V <sub>CC</sub> = 6.0 V			83	ns/V

## 9. Static Characteristics

**Table 6. Static characteristics**

At recommended operating conditions. Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	-40 °C to +85 °C			-40 °C to +125 °C		Unit
			Min	Typ[1]	Max	Min	Max	
V <sub>IH</sub>	HIGH-level input voltage	V <sub>CC</sub> = 2.0 V	1.5	1.26		1.5		V
		V <sub>CC</sub> = 4.5 V	3.15	2.48		3.15		V
		V <sub>CC</sub> = 6.0 V	4.2	3.22		4.2		V
V <sub>IL</sub>	LOW-level input voltage	V <sub>CC</sub> = 2.0 V		0.84	0.5		0.5	V
		V <sub>CC</sub> = 4.5 V		1.99	1.35		1.35	V
		V <sub>CC</sub> = 6.0 V		2.65	1.8		1.8	V
V <sub>OH</sub>	HIGH-level output voltage	V <sub>I</sub> = V <sub>IH</sub> or V <sub>IL</sub>						
		I <sub>O</sub> = -20 μA; V <sub>CC</sub> = 2.0 V	1.9	2.0		1.9		V
		I <sub>O</sub> = -20 μA; V <sub>CC</sub> = 4.5 V	4.4	4.5		4.4		V
		I <sub>O</sub> = -20 μA; V <sub>CC</sub> = 6.0 V	5.9	6.0		5.9		V
		I <sub>O</sub> = -4.0 mA; V <sub>CC</sub> = 4.5 V	3.84	4.38		3.7		V
		I <sub>O</sub> = -5.2 mA; V <sub>CC</sub> = 6.0 V	5.34	5.87		5.2		V
V <sub>OL</sub>	LOW-level output voltage	V <sub>I</sub> = V <sub>IH</sub> or V <sub>IL</sub>						
		I <sub>O</sub> = 20 μA; V <sub>CC</sub> = 2.5 V		0	0.1		0.1	V
		I <sub>O</sub> = 20 μA; V <sub>CC</sub> = 4.5 V		0	0.1		0.1	V
		I <sub>O</sub> = 20 μA; V <sub>CC</sub> = 6.0 V		0	0.1		0.1	V
		I <sub>O</sub> = 4.0 mA; V <sub>CC</sub> = 4.5 V		0.08	0.33		0.4	V
		I <sub>O</sub> = 5.2 mA; V <sub>CC</sub> = 6.0 V		0.09	0.33		0.4	V
I <sub>I</sub>	input leakage current	V <sub>I</sub> = V <sub>CC</sub> or GND ; V <sub>CC</sub> = 6.0 V		0.003	±1		±1	μA
I <sub>CC</sub>	supply current	V <sub>I</sub> = V <sub>CC</sub> or GND ; I <sub>O</sub> = 0A ; V <sub>CC</sub> = 6.0 V		12.7	20		40	μA
C <sub>I</sub>	input capacitance			1.5				pF

 [1]All typical values are measured at T<sub>amb</sub> = 25°C.

## 10. Dynamic Characteristics

**Table 7. Dynamic characteristics**

Voltages are referenced to GND (ground = 0 V); for test circuit see Fig. 9.

Symbol	Parameter	Conditions	-40 °C to +85 °C			-40 °C to +125 °C		Unit
			Min	Typ[1]	Max	Min	Max	
$t_{pd}$	propagation delay	CP to Qn; see Fig. 6 [2]						
		$V_{CC} = 2.0\text{ V}$		20	30		35	ns
		$V_{CC} = 4.5\text{ V}$		8.5	15		20	ns
		$V_{CC} = 6.0\text{ V}$		7.4	10		15	ns
$t_{PHL}$	HIGH to LOW propagation delay	$\overline{MR}$ to Qn; see Fig. 7						
		$V_{CC} = 2.0\text{ V}$		20	30		35	ns
		$V_{CC} = 4.5\text{ V}$		9	15		20	ns
		$V_{CC} = 6.0\text{ V}$		7.6	10		15	ns
$t_t$	transition time	see Fig. 6 [3]						
		$V_{CC} = 2.0\text{ V}$		10	15		20	ns
		$V_{CC} = 4.5\text{ V}$		4.2	10		15	ns
		$V_{CC} = 6.0\text{ V}$		4	7		9	ns
$t_w$	pulse width	CP HIGH or LOW; see Fig. 6						
		$V_{CC} = 2.0\text{ V}$	100			120		ns
		$V_{CC} = 4.5\text{ V}$	20			24		ns
		$V_{CC} = 6.0\text{ V}$	17			20		ns
		$\overline{MR}$ LOW; see Fig. 7						
		$V_{CC} = 2.0\text{ V}$	75			90		ns
		$V_{CC} = 4.5\text{ V}$	15			18		ns
		$V_{CC} = 6.0\text{ V}$	13			15		ns
$t_{rec}$	recovery time	$\overline{MR}$ to CP; see Fig. 7						
		$V_{CC} = 2.0\text{ V}$	75			90		ns
		$V_{CC} = 4.5\text{ V}$	15			18		ns
		$V_{CC} = 6.0\text{ V}$	13			15		ns

## EM74HC164A

### 8-bit serial-in, parallel-out shift register

Symbol	Parameter	Conditions	-40 °C to +85 °C			-40 °C to +125 °C		Unit
			Min	Typ[1]	Max	Min	Max	
t <sub>su</sub>	set up time	DSA and DSB to CP; see Fig. 8						
		V <sub>CC</sub> = 2.0 V	75			90		ns
		V <sub>CC</sub> = 4.5 V	15			18		ns
		V <sub>CC</sub> = 6.0 V	13			15		ns
t <sub>h</sub>	hold time	DSA and DSB to CP; see Fig. 8						
		V <sub>CC</sub> = 2.0 V	4			4		ns
		V <sub>CC</sub> = 4.5 V	4			4		ns
		V <sub>CC</sub> = 6.0 V	4			4		ns
f <sub>max</sub>	maximum frequency	for CP; see Fig. 6						
		V <sub>CC</sub> = 2.0 V	5			4		MHz
		V <sub>CC</sub> = 4.5 V	24			20		MHz
		V <sub>CC</sub> = 6.0 V	28			24		MHz
C <sub>PD</sub>	power dissipation capacitance	per package ; V <sub>I</sub> = GND to V <sub>CC</sub> ; [4]		25				pF

[1] Typical values are measured at T<sub>amb</sub> = 25 °C.

[2] t<sub>pd</sub> is the same as t<sub>PLH</sub> and t<sub>PHL</sub>.

[3] t<sub>t</sub> is the same as t<sub>THL</sub> and t<sub>THL</sub>.

[4] C<sub>PD</sub> is used to determine the dynamic power dissipation (P<sub>D</sub> in μW).

$P_D = C_{PD} \times V_{CC}^2 \times f_i \times N + \sum(C_L \times V_{CC}^2 \times f_o)$  where:

f<sub>i</sub> = input frequency in MHz;

f<sub>o</sub> = output frequency in MHz;

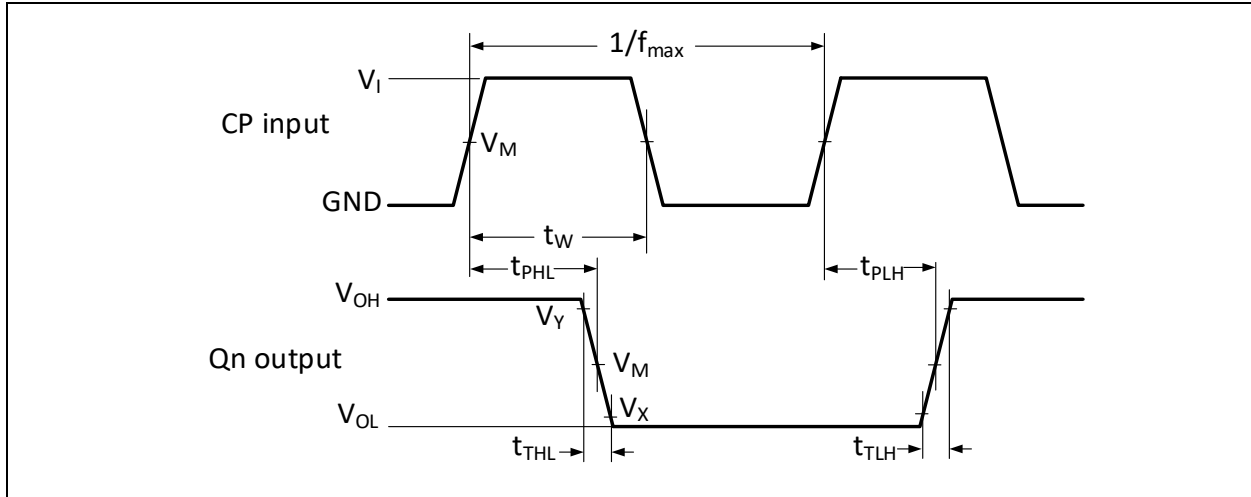
C<sub>L</sub> = output load capacitance in pF;

V<sub>CC</sub> = supply voltage in V;

N = number of inputs switching;

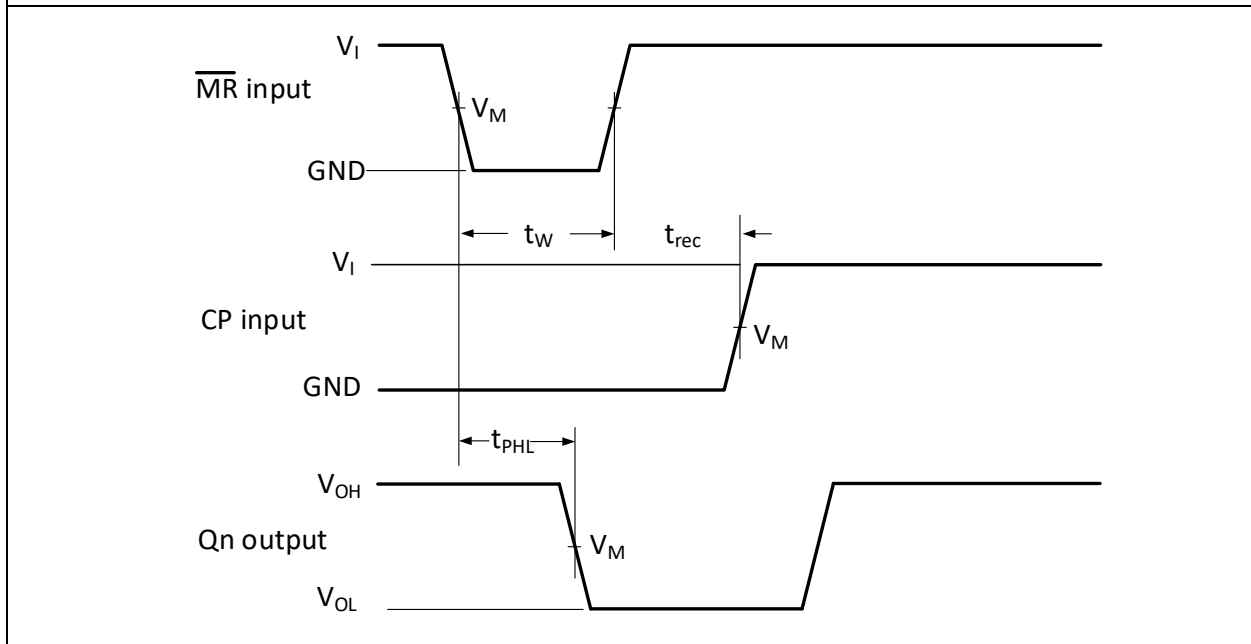
$\sum(C_L \times V_{CC}^2 \times f_o)$  = sum of outputs.

### 10.1. Waveforms and test circuit



Measurement points are given in Table 8.  
 $V_{OL}$  and  $V_{OH}$  are typical output voltage levels that occur with the output load.

Fig. 6. Waveforms showing the clock (CP) to output (Qn) propagation delays, the clock pulse width, the output transition times and the maximum clock frequency

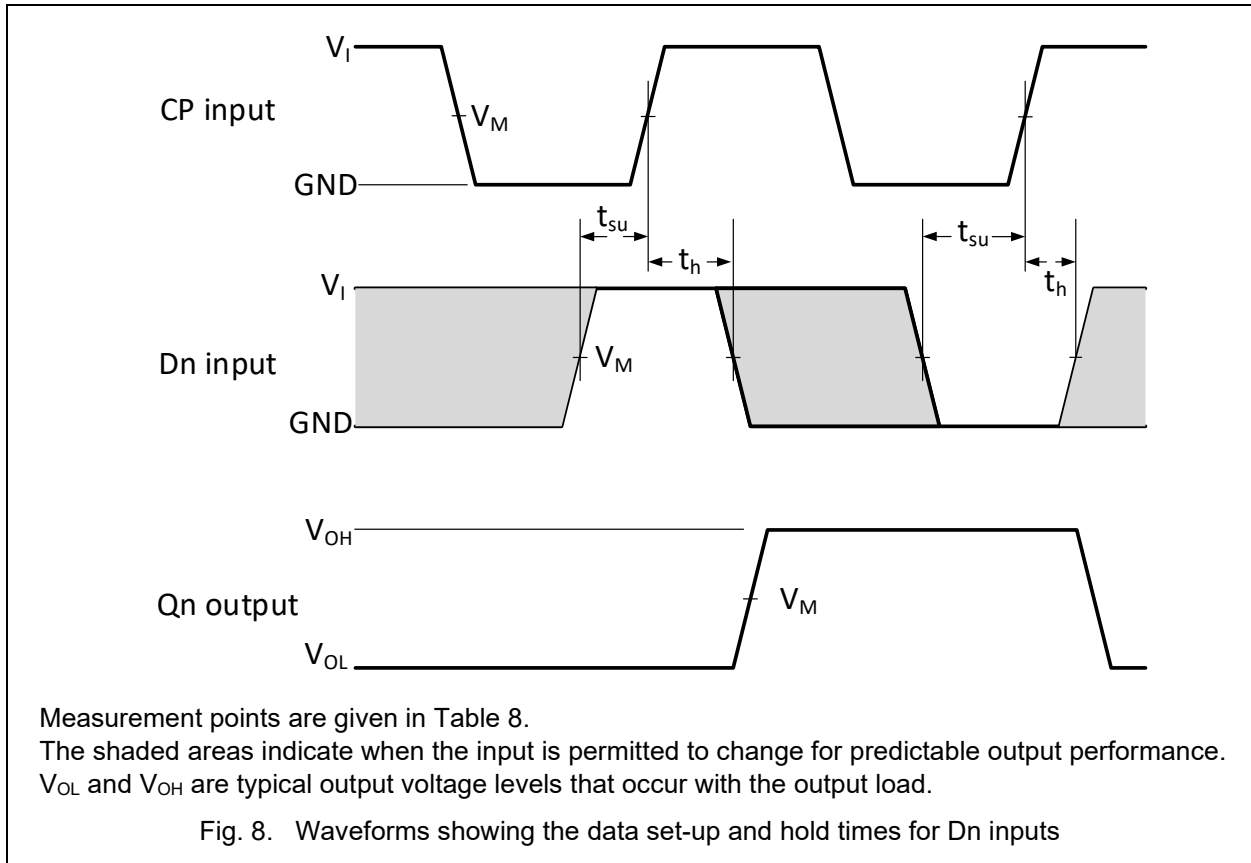


Measurement points are given in Table 8.  
 $V_{OL}$  and  $V_{OH}$  are typical output voltage levels that occur with the output load.

Fig. 7. Waveforms showing the master reset ( $\overline{MR}$ ) pulse width, the master reset to output (Qn) propagation delays and the master reset to clock (CP) removal time

# EM74HC164A

## 8-bit serial-in, parallel-out shift register

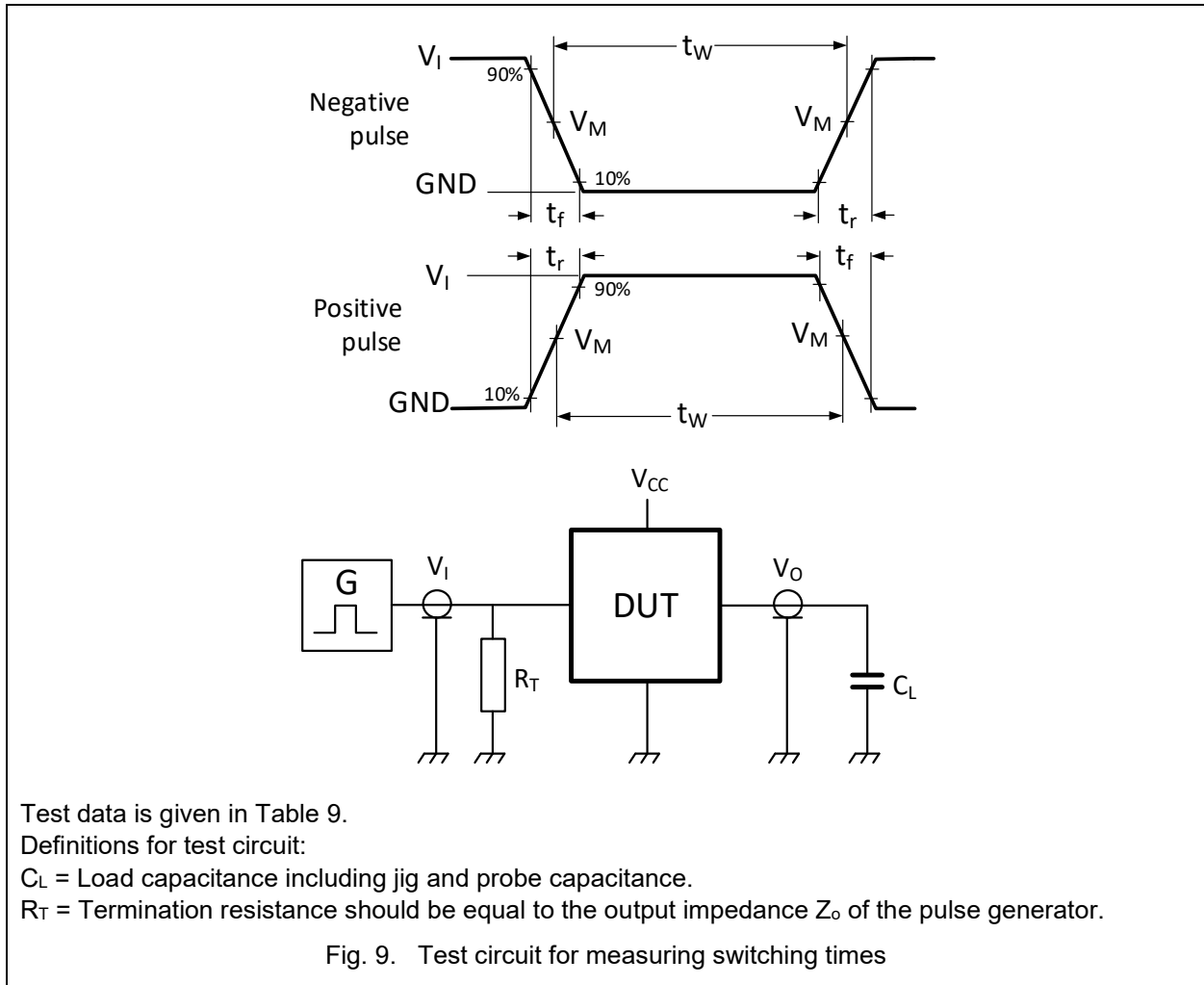


**Table 8. Measurement points**

Type	Input	Output		
	$V_I$	$V_M$	$V_X$	$V_Y$
EM74HC164A	$0.5V_{CC}$	$0.5V_{CC}$	$0.1V_{CC}$	$0.9V_{CC}$

# EM74HC164A

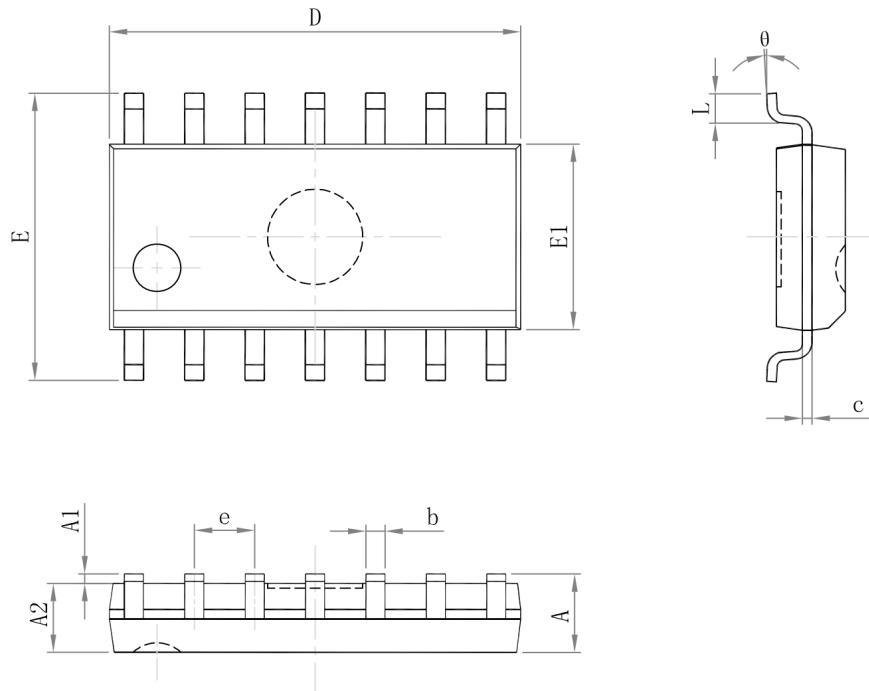
## 8-bit serial-in, parallel-out shift register



**Table 9. Test data**

Type	Input		Load	Test
	$V_I$	$t_r = t_f$	$C_L$	
EM74HC164A	$V_{CC}$	2.5 ns	50 pF	$t_{PHL}, t_{PLH}$

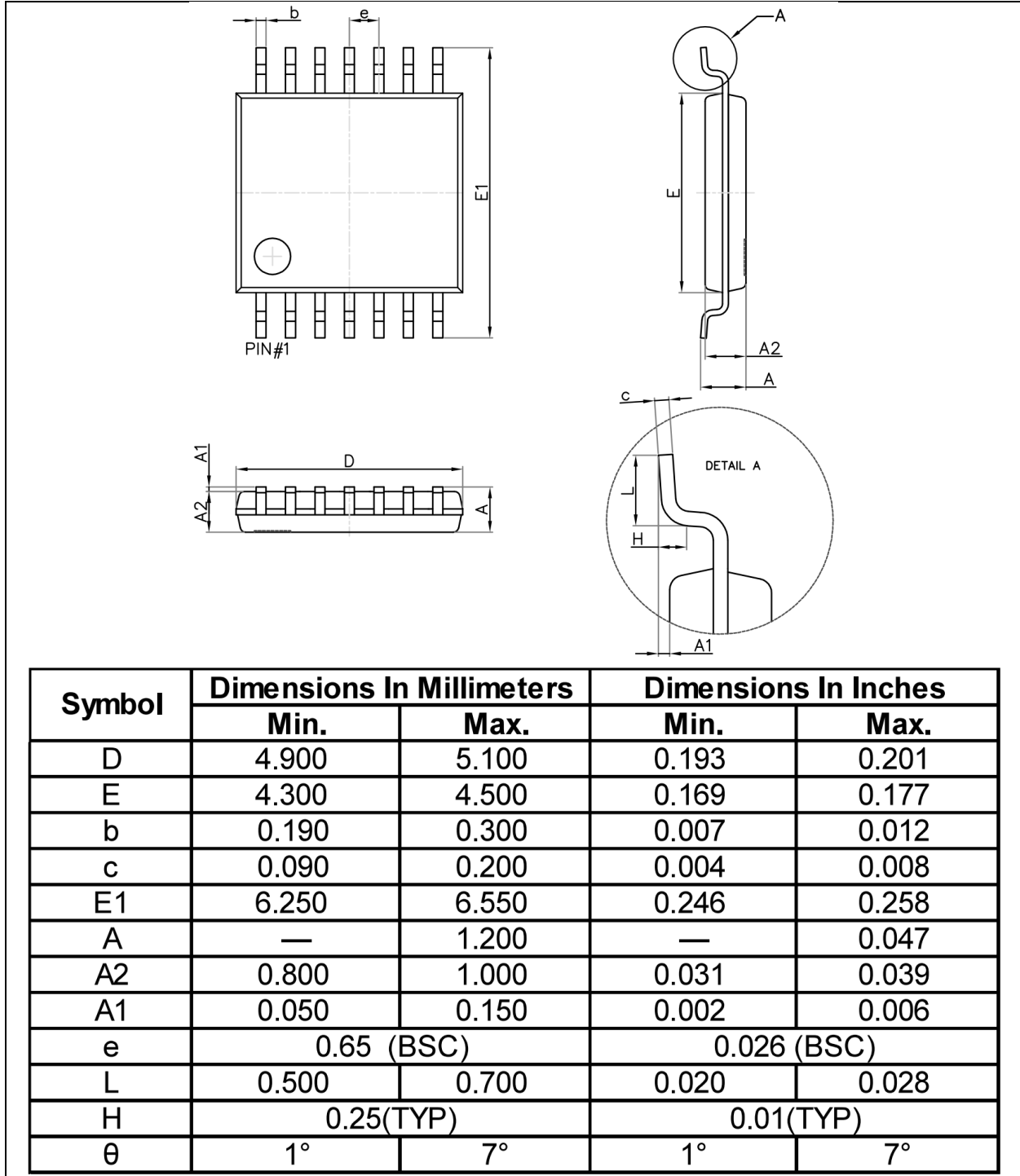
# 11. Package Outline

**SOP-14L**


Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	--	1.750	--	0.069
A1	0.100	0.250	0.004	0.010
A2	1.250	--	0.049	--
b	0.310	0.510	0.012	0.020
c	0.100	0.250	0.004	0.010
D	8.450	8.850	0.333	0.348
E	5.800	6.200	0.228	0.244
E1	3.800	4.000	0.150	0.157
e	1.270(BSC)		0.050(BSC)	
L	0.400	1.270	0.016	0.050
θ	0°	8°	0°	8°

**EM74HC164A**

8-bit serial-in, parallel-out shift register

**TSSOP-14L**


## 12. Tape and Reel Information

### 12.1. Carrier tape dimensions

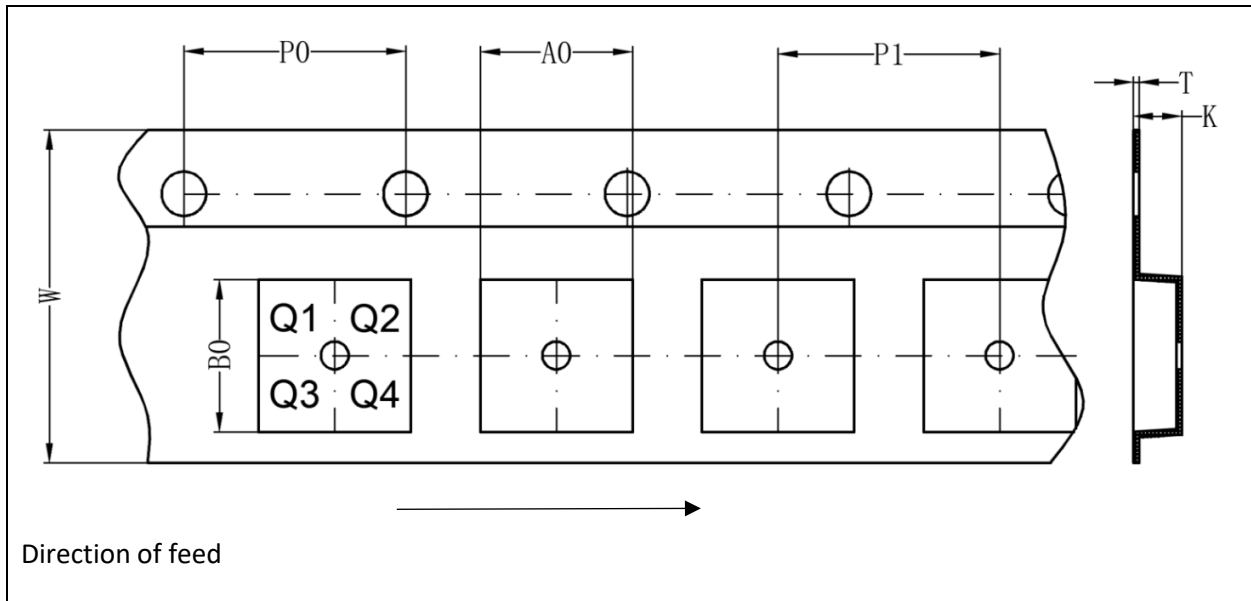


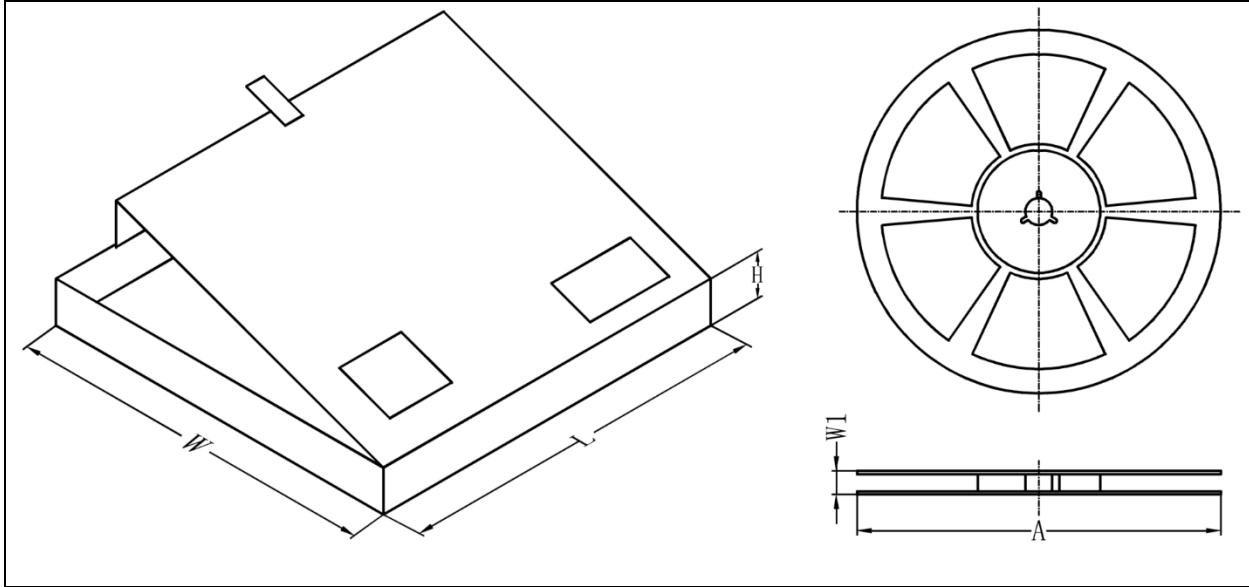
Table 10. Carrier tape dimensions

Package version	A0(mm)	B0(mm)	K0(mm)	T(mm)	P1(mm)	W(mm)	P0(mm)	PIN 1
SOP-14L	6.35	9.1	1.9	0.22	8	16	4	Q1
TSSOP-14L	6.7	5.45	1.6	0.25	8	12	4	Q1

## EM74HC164A

8-bit serial-in, parallel-out shift register

### 12.2. Reel and box dimensions



**Table 11. Dimensions and quantities**

Package version	Type NO. ending	Reel Dimension A (mm)	Max Reel Width W1 (mm)	SPQ (pcs)[1]	Reels per box	Outer box dimensions L×W×H(mm)[2]
SOP-14L	D	330	22.4	3000	1	358x340x50
TSSOP-14L	PW	330	18.4	3000	1	358x340x50

[1] Packing quantity dependent on specific product type. Please contact your local Energymath representative for ordering.

[2] Dimensions for reference only.

## 13. Abbreviations

Table 12. Abbreviations

Acronym	Description
CMOS	Complementary Metal-Oxide Semiconductor
DUT	Device Under Test
ESD	ElectroStatic Discharge
HBM	Human Body Model
CDM	Charged Device Model

## 14. Revision History

Table 13. Revision history

Document ID	Release Date	Data sheet status	Change notice	Supersedes
EM74HC164A Rev. 1.1	Jun 15, 2025	Product datasheet		EM74HC164A Rev. 1.0
Modifications:	• Section 12 : Added tape and reel information.			
EM74HC164A Rev. 1.0	Jul 07, 2024	Product datasheet		